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L1	130	((email or (electronic adj mail)) and cach$5).clm.	US-PGPUB	OR	OFF	2007/01/29 09:19
L2	20	((email or (electronic adj mail)) same cach$5).clm.	US-PGPUB	OR	OFF	2007/01/29 09:19
L3	4	(initiat$3 same (email or (electronic adj mail)) same trigger).clm.	US-PGPUB	OR	OFF	2007/01/29 09:20
L4	0	(cach$5 same (email or (electronic adj mail)) same trigger).clm.	US-PGPUB	OR	OFF	2007/01/29 09:20
L5	5	(cach$5 and (email or (electronic adj mail)) and trigger).clm.	US-PGPUB	OR	OFF	2007/01/29 09:20
L6	0	((email or (electronic adj mail)) and (cache near5 hierarchical)).clm.	US-PGPUB	OR	OFF	2007/01/29 09:20
L7	12	((email or (electronic adj mail)) same cache).clm.	US-PGPUB	OR	OFF	2007/01/29 09:21

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S1	11881	(email or (electronic adj mail)) and cach$5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 15:40
S2	904	(email or (electronic adj mail)) same cach$5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 11:28
S3	14	(email or (electronic adj mail)) same cach$5 same trigger	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 11:37
S4	1178	(email or (electronic adj mail))same trigger	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 11:37
S5	1178	(email or (electronic adj mail)) same trigger	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 11:38
S6	0	initialt$3 same (email or (electronic adj mail)) same trigger	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 11:38
S7	134	initiat$3 same (email or (electronic adj mail)) same trigger	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:06

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S8	1491	(711/118).CCLS.	USPAT; USOCR	OR	OFF	2007/01/25 14:18
S9	22	(email or (electronic adj mail)) and S8	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:53
S10	1356	(709/220).CCLS.	USPAT; USOCR	OR	OFF	2007/01/25 14:31
S11	823	(709/221).CCLS.	USPAT; USOCR	OR	OFF	2007/01/25 14:31
S12	547	(709/222).CCLS.	USPAT; USOCR	OR	OFF	2007/01/25 14:31
S13	2640	(709/219).CCLS.	USPAT; USOCR	OR	OFF	2007/01/25 14:31
S14	2977	(709/217).CCLS.	USPAT; USOCR	OR	OFF	2007/01/25 14:31
S15	533	(709/248).CCLS.	USPAT; USOCR	OR	OFF	2007/01/25 14:31
S16	11881	(email or (electronic adj mail)) and cach$5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:32
S17	66	S10 and S16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:32
S18	36	S11 and S16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:32
S19	15	S12 and S16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:32

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S20	253	S13 and S16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:32
S21	249	S14 and S16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:32
S22	30	S15 and S16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:32
S23	904	(email or (electronic adj mail)) same cach$5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:32
S24	8	S10 and S23	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:32
S25	3	S11 and S23	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:33
S26	2	S12 and S23	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:34

EAST Search History

S27	24	S13 and S23	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:34
S28	28	S14 and S23	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:38
S29	4	S15 and S23	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:40
S30	527	(email or (electronic adj mail)) same cache	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:54
S31	1	(email or (electronic adj mail)) same cache same progressive	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:58
S32	2893	(progressive hierarch$5) near cach$5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:55
S33	21	((progressive hierarch$5) near cach$5) same metadata	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:56

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S34	1	(email or (electronic adj mail)) same (cache near5 hierarchical)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:58
S35	49	(email or (electronic adj mail)) and (cache near5 hierarchical)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/25 14:59
S36	1	("6990558").PN.	USPAT; USOCR	OR	OFF	2007/01/25 15:44
S37	1	("6138213").PN.	USPAT; USOCR	OR	OFF	2007/01/25 15:45

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1 Intelligent database caching through the use of page-answers and page-traces

Nabil Kamel, Roger King
December 1992 **ACM Transactions on Database Systems (TODS)**, Volume 17 Issue 4
Publisher: ACM Press

Full text available: pdf(3.08 MB) Additional Information: full citation, abstract, references, citings, index terms

In this paper a new method to improve the utilization of main memory systems is presented. The new method is based on prestoring in main memory a number of query answers, each evaluated out of a single memory page. To this end, the ideas of page-answers and page-traces are formally described and their properties analyzed. The query model used here allows for selection, projection, join, recursive queries as well as arbitrary combinations. We also show how to apply the approach under update ...

Keywords: artificial intelligence, databases, page access

2 Using prediction to accelerate coherence protocols

Shubhendu S. Mukherjee, Mark D. Hill
April 1998 **ACM SIGARCH Computer Architecture News , Proceedings of the 25th annual international symposium on Computer architecture ISCA '98**, Volume 26 Issue 3
Publisher: IEEE Computer Society, ACM Press

Full text available: pdf(1.71 MB) Publisher Site Additional Information: full citation, abstract, references, citings, index terms

Most large shared-memory multiprocessors use directory protocols to keep per-processor caches coherent. Some memory references in such systems, however, suffer long latencies for misses to remotely-cached blocks. To ameliorate this latency, researchers have augmented standard coherence protocols with optimizations for specific sharing patterns, such as read-modify-write, producer-consumer, and migratory sharing. This paper seeks to replace these directed solutions with general prediction logic t ...

3 Selective-set-invalidation (SSI) for soft-error-resilient cache architecture

Seung H. Hwang, Gwan S. Choi
June 1999 **ACM SIGARCH Computer Architecture News**, Volume 27 Issue 3
Publisher: ACM Press

Full text available: pdf(489.77 KB) Additional Information: full citation, abstract, index terms

This paper proposes a novel cache-memory design for soft-error silence, and verifies the design through a simulation that uses realistic system and software model. The SSI design is a combination of an n-bit error detector and a fast circuit that allows real-time-forced invalidation of corrupted data sets. The current design supports the write through caching policy and will be extended for the write back policy. To verify the effectiveness of the proposed design approach, mixed-mode simulations ...

4 Code compression for embedded systems

Haris Lekatsas, Wayne Wolf

May 1998 **Proceedings of the 35th annual conference on Design automation DAC '98**

Publisher: ACM Press

Full text available: pdf(414.41 KB) Additional Information: full citation, abstract, references, citings, index terms

Memory is one of the most restricted resources in many modern embedded systems. Code compression can provide substantial savings in terms of size. In a compressed code CPU, a cache miss triggers the decompression of a main memory block, before it gets transferred to the cache. Because the code must be decompressible starting from any point (or at least at cache block boundaries), most file-oriented compression techniques cannot be used. We propose two algorithms to compress code in ...

Keywords: MPEG4, codec, design automatian, flip-flops, level converters, low power, placement, synthesis, voltage scaling

5 Filtering algorithms and implementation for very fast publish/subscribe systems

Françoise Fabret, H. Arno Jacobsen, François Llirbat, João Pereira, Kenneth A. Ross, Dennis Shasha

May 2001 **ACM SIGMOD Record , Proceedings of the 2001 ACM SIGMOD international conference on Management of data SIGMOD '01**, Volume 30 Issue 2

Publisher: ACM Press

Full text available: pdf(315.41 KB) Additional Information: full citation, abstract, references, citings, index terms

Publish/Subscribe is the paradigm in which users express long-term interests ("subscriptions") and some agent "publishes" events (e.g., offers). The job of Publish/Subscribe software is to send events to the owners of subscriptions satisfied by those events. For example, a user subscription may consist of an interest in an airplane of a certain type, not to exceed a certain price. A published event may consist of an offer of an airplane with certain properties includin ...

6 Web server workload characterization: the search for invariants

Martin F. Arlitt, Carey L. Williamson

May 1996 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1996 ACM SIGMETRICS international conference on Measurement and modeling of computer systems SIGMETRICS '96**, Volume 24 Issue 1

Publisher: ACM Press

Full text available: pdf(1.28 MB) Additional Information: full citation, abstract, references, citings, index terms

The phenomenal growth in popularity of the World Wide Web (WWW, or the Web) has made WWW traffic the largest contributor to packet and byte traffic on the NSFNET backbone. This growth has triggered recent research aimed at reducing the volume of network traffic produced by Web clients and servers, by using caching, and reducing the latency for WWW users, by using improved protocols for Web interaction.Fundamental to the goal of improving WWW performance is an understanding of WWW workloads. This ...

7 An analysis of wide-area name server traffic: a study of the Internet Domain Name System

Peter B. Danzig, Katia Obraczka, Anant Kumar

October 1992 **ACM SIGCOMM Computer Communication Review , Conference proceedings on Communications architectures & protocols SIGCOMM '92**, Volume 22 Issue 4

Publisher: ACM Press

Full text available: pdf(1.19 MB) Additional Information: full citation, abstract, references, citings, index terms

Over a million computers implement the Internet's Domain Name System of DNS, making it the world's most distributed database and the Internet's most significant source of wide-area RPC-like traffic. Last year, over eight percent of the packets and four percent of the bytes that traversed the NSFnet were due to DNS. We estimate that a third of this wide-area DNS traffic was destined to seven root name servers. This paper explores the performance of DNS based on two 24-hour t ...

8 Runtime optimizations for a Java DSM implementation

R. Veldema, R. F. H. Hofman, R. A. F. Bhoedjang, H. E. Bal

June 2001 **Proceedings of the 2001 joint ACM-ISCOPE conference on Java Grande JGI '01**

Publisher: ACM Press

Full text available: pdf(740.71 KB) Additional Information: full citation, abstract, references, citings, index terms

Jackal is a fine-grained distributed shared memory implementation of the Java programming language. Jackal implements Java's memory model and allows multithreaded Java programs to run unmodified on distributed-memory systems.

This paper focuses on Jackal's runtime system, which implements a multiple-writer, home-based consistency protocol. Protocol actions are triggered by software access checks that Jackal's compiler inserts before object and array references. We describe optimizatio ...

9 Statistical language modeling: Improvements in stochastic language modeling

Ronald Rosenfeld, Xuedong Huang

February 1992 **Proceedings of the workshop on Speech and Natural Language HLT '91**

Publisher: Association for Computational Linguistics

Full text available: pdf(491.53 KB) Additional Information: full citation, abstract, references, citings

We describe two attempt to improve our stochastic language models. In the first, we identify a systematic overestimation in the traditional backoff model, and use statistical reasoning to correct it. Our modification results in up to 6% reduction in the perplexity of various tasks. Although the improvement is modest, it is achieved with hardly any increase in the complexity of the model. Both analysis and empirical data suggest that the modification is most suitable when training data is sparse. ...

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31 Jan.-4 Feb. 2005 Page(s):370 - 376
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System Sciences, 1999. HICSS-32. Proceedings of the 32nd Annual Hawaii In Conference on
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